TATA ELXSI PAPER ON 2nd MAY AT GUJARAT

Hi, friends...........there are three stages in selection procedure of tata elxsi..

1 written test.
2 technical interview.
3 hr interview.

it was off campus here at ddu nadiad and it was for computer , EC and electronics...on
first day there were written test . in that there were 4 section and there was NO Negative
marking and about 50% Sectional cutoff

in written test,
1,English section 20 questions and 20 min.
it was easy means if you know basics of articals then no need to worry.

2,Aptitude section 20 q. and 25min.
it was of simple calculations , cube problems , and 5 logical reasoning it was mediam

3,Calculation section 25 q. and 25 min.
it was simplest q. like @=0 and $=1 and then covert 25 into binary and represent it with
this symbols
then 1233465 1233465 1233466 find out odd one .
So it was very easy...........

4,technical section 30 q. and 30 min.
In this there were separate papers for CS AND EC. i am from CS so in this there were q.
from c ,c++ ,networking ,few from operating system,and many from data structures.. so
be well prepare for data structures and networking .....

they declared result at 4:30 and the technical interview of about 30 students was on that
day itself. while for other 35 it was on next day morning.....

IN TECHNICAL
only one member was there on starting he told me to give resume
then told me to list out subjects i studied ..
then he asked many q. from networking means what are uses of networking?
what is subnet ? about class of ip address...
then asked from c and c++ means diff. between structure and union ?
from array and linked list ....some from pointers ...
and at last he asked from opetrating sys. but i told him about my project so he didnt asked
much from os.
and finally one mem told me that you are selected for HR round and you have to be
present on tom. morning 9 .

HR interview
Mostly they dont discard people from this round because the technical was very tuff so be confident and tell any thing means wheather it is true or false .IN my intreview there was one mem who asked me in a very good way that i was very relax
she asked discribe your self...
About your family...
stenghths and weakness ..
in this tell some weakness and also tell how you are improving it ..
about tata elxsi..
some about aims ....

Finally she gave me one long form and told me congo...so now i am part of tata elxsi......
BEST OF LUCK......................

By,
Viraj Tank

Tata Elxsi Questions (Pattern Paper)

1.   Problem on lissojous pattern: in x-y mode if vlg applied to x-axis is 2 sinwt then what is the vtg to be applied to y-axis so that we observe the following pattern on the cro : [angle b/n x-axis and the diagonal line is 45degrees] ans is 2root 2 sin (wt +45)

2.   What is the resolution of an adc : vref/(2 pow n).

3.   Some circuit is given with two f/f s ,what is that circuit? Ans may be shift reg

4.   Spatial to temporal converter is ? (a) shift reg (b) counter (c) adc ans may be adc

5.   Darlington is used for _____ ans high current gain

6.   Some circuit with nand gates is given ,what is that circuit ans is xor

7.   A wired circuit with some nand gates is given asked the function of that circuit

8.   An opamp circuit is given asked to find the transfer function of it (I think its an ideal integrator circuit)

9.   A pole zero pattern of an impedance function is given . it has a conjugate pair of zeros and a conjugate pair of poles ,then what elements does the impedance consists of? Ans LC elements

10.   Most computers wont use floating point arithmetic .why   a.costly b. slow c. cant

11.   Bit-slice processor mean …… ans used for shift ,masking of bits

12.   ac motor used in servo applications is (a) single phase induction b.2 phase induction c. 3 phase induction

13.   TV resolution depends on a. bandwidth 2. detector c. no. of frames /sec

14.   Equalizing pulses are sent in tv during a. horizontal blanking b. vertical blanking ……….

15.   cdma is not suitable for satellite communication . why ? ans may be complexity of operation

16.   Given some waveforms and asked that which of the waveforms will have odd harmonics of sine terms

17.   Asked for two's complement of a number

18.   maximum signed no. represented by a 8 bit computer

19.   A chip has a capacity of 256bits,to implement a 1KB capacity how many chips are required ans 32

20.   ecl operates in _____ and _____ regions of the transistor

21.   Given a transient circuit ,asked for time constant while charging and discharging

22.   He had given 4 statements about a f/f and asked which of them are correct

1. it can store a bit 2. jk f/f have rat race when storing a 2 bit info 3. master slave is used to store a 2 bit info

4. d f/f used as a latch for multiplexing operations

23.   Venn diagram is given and asked to write expression for the shared portion in it

24.   cb and ce are _____ biased when transistor is in saturation

25.   condition for nyquist rate

26.   carry look ahed adder is 1.fast 2. requires more h/w 3. costly

27.   ideal power supply will have a. zero internal resistance b. more o/p resistance c. less o/p resistance

28.   A problem on cro and probe interface i.e design of an attenuator for the cro probe (formula for it is R1 * c1 = R2 * C2)

TATA ELXSI PAPER ON 27th JULY AT CHENNAI

Company : Tata Elxsi
Date : 27th July 2007

Hi, This is Sankha Ray. Pursuing M.Tech in Computer Science in SRM Engg. College, Chennai. On 27th July Tata Elxsi conducted placement recruitment on campus. It is an Electonics based company..Go through its wesite www.tataelxsi.com to get detailed knowledge about this company. Nearly 400 students appeared in written test,conducted by Meritrac. The test was 100 mins, 100 marks,100 ques.
4 sections.

1. Verbal: 20mins 25 ques
Articles, Prepositions etc..dead easy.
Reading Passage..

2. Aptitude: 25 mins 25ques
Go through R.S.Agarwal.
a) problem like.. + replaced by /, / replaced by -,...
b) $ represents 1, * represents 0...
c) Venn Diagram..6 questions
d) Cube Problem..5 questions..
A cube is divided into 125 pieces.. then four columns are removed then coloured all side black.. ans this ques..
i) how many 0 side painted cubes? 27
ii) how many 1 side painted cubes? 30
iii) how many 2 side painted cubes? 32
iV) how many 3 side painted cubes? 16
V) how many 4 side painted cubes? 0

3. ATTENTION TO DETAILS.. 25mins 20 ques.

Go through R.S. Aggarwal..
problem like..
i) represents if options A is true.
ii) represents if options B is true.
iii) represents if options either A or B is true.
iV) represents if options neither A nor B is true.
V) represents if options both A & B is true.

Problem : Some text.....
Conclusions
A) Some text.....
B) Some text.....

4. Technical .. 30 mins 30 ques
Questions about C, datastructure, digital elecronics.micro procesor,
Out of 400 only 130 stuents cleared the test.

They announced this day itself they will call Electronics and circuit based branches.. next
day on 28th Computer Sc, IT have to come..
So my interview was on the next day..they were concentrating only on Micro
Processor..So i studied micro processors basics..

TECHNICAL ROUND:
Remember this is the main eleminating round..My interview was at nearly at 6pm. I was
waiting since 9am. I entered into the interview panel.
Only one person was there in the panel.. He told to sit down..
I was fully confident that day, i dont know why?..but my confidence help me a lot to get
the job..he asked my resume..
I wrote my tech.skills as C, Php, Java, etc.. that persons asked me all the basics about
only C..i was very lucky..bcoz i am strong in C..
questions like..
a) deault Access specifier of struct & class in C++.
b) difference b/w struct & class,
c) NULL pointer.
d) structure padding..(i didn't answered)
e) dangling pointer...(i didn't answered)
f) storage classes..
g) static & auto
h) array, linklist, singly linklist, doubly linklist.
i) advantages of doubly over singly linklist.
j) define DBMS.
k) SQL query.
j) diffrence between star & ring topology..
l) what will be the output..
   #define MAX 100
   void main() {
    printf("%f", MAX);

    }
etc...

I cleared the interview.. dont think that all the questions will be asked as simple as i had..thats why i already said i was lucky..other students was asked difficult ques..like Micro Processor, Digital Electrnics..etc..

Amoung 130 students 40 students cleared the Technical interview..
then i went to HR.
these questions were aked to me..
1) why did u get less marks in 10 & +2...
2) why are u pursuing MTECH?
3) what u know about TATA Elxsi?? here i suggest u go through the site and gather what ever info given there in a brief way. listen carefully to their Pre Placement Talk(PPT). try dont to stop till they fire another question to u.
4) what package we are providing?
5) Do u know about the bond? there is 2yr Bond.if broken we have to pay 3lakhs.
6) have any problem with that?
7) parents will agree with the relocation of job?
thats all

He (HR) give me a form to fill up and to be submitted to him..Again they told ur short will be short listed again..they announced my name. 24 stuents were selected totally..Training 3months in Bangalore..

BEST OF LUCK..

TATA ELXSI PLACEMENT PAPERS 2007

TATA ELXSI PAPER ON 17th APRIL

Hai Friends,
I have recently been placed in Tata Elxsi. This is an electronics based company, one that is very good for engineering students who does not want to get into software industry. The selection procedure is very strict. They want students who are really sound in their departmental subjects. Only CS ,ECE students are allowed. No IT

1. Aptitude Test
The aptitude consists of two sections.
1. 30 question Reasoning
2. 30 questions Technical (Separate questions for EC and CS)

The reasoning part consist of questions from "profit and loss", Age related problems", "Clock", "Calender", "Time and work", I think "Logical reasoning" by R.S. Agarwaal

will be a good text book for the preparation of this part.
I am an EC student, for me there were questions from LIC, DE, C, C++, and from Basic
Electronics for the technical test .

About 250+ students appeared for the aptitude test and only 89 were selected for the
technical interview.

2. Technical Interview
It was the toughest section in the whole selection procedure . It start like a normal HR
Interview, Then he asked questions from Microprocessors, Digital Electronics, Basic
electronics, LIC, Control systems , DSP ie almost all subjects we have to study upto 6
semester. I performed well in this section. I answered almost all the questions he asked.
The technical interview for me lasted for 1.5hrs. I got a form from there, that means i got
cleared from that section.

About 29 students were selected for HR interview.

3. HR Interview
That was a cool section. They asked normal HR questions like self intro, hobbies,
strengths and weakness,etc. It lasted for half an hour. Finally the result announced by
God's grace I got the selection.

TATA ELXSI PAPER ON 15th APRIL AT CHANDIGARH

Hi guys!!!!!!! This is Preeti Goyal from chandigarh!!!!!!
I appeared for TATA ELXSI placement paper via joint campus placement. There were
around 5000candidates who appeared and just 20 got through at the end. It was via g4
solutions so was more tough as there were 5rounds to be faced.

1st:-ONLINE TEST
firstly,there was an online test of g4 solutions includin aptitude,englisg,rc and technical
according to ur branch(ece or cse). technical was tough in my paper.60 ques.in 60min. be
fast. n attempt easy. aptitude was easy.

2nd:-GD
those who cleared online paper had to go for gd.there were group of 10 students
after these two rounds just 250 got selected for further procedure by the company itself.

3rd:-WRITTEN TEST
it was again a written test consisting of four sections in 100min. 100ques. it included
english,di,attention to details and again technical.no negative marking.

4th:-TECHNICAL INTERVIEW
around 60 were left now. now v had to face the most diifficult of all rounds......the

technical interview. they were asking everythin under the sun........being fron ece i was asked everything from C++, MICROPROCESSOR, MICROCONTROLLER, D.E,EDC, OP-AMP'S

And most importantly project done in 6week training. My interview lasted for 45min.

5th:-HR INTERVIEW
nw was hr interview.just a formality........man u hav done it!

TATA ELXSI PAPER ON 18th MARCH AT KOLKATA

Hi,
I have recently been placed in Tata Elxsi.this is an electronics based company,one that is very good for engineering students who does not want to get into software industry.

The selection procedure is very strict. They want students who are really sound in their departmental subjects. Only CS ,ECE students are allowed. No IT.

1.Aptitude test: It is conducted by merittrac. They have separate papers for verbal(25 questions 20 mins),analytical,quantitative(20 questions 20 mins) & technical(30 questions 30 mins).In technical they have separate papers for CS & ECE.For ECE questions are mainly from digital electronics, analog electronics, microprocessor, circuit theory, em theory etc.There is sectional cutoff for each section.I personally don't think cutoff is very high.If u can attempt 15 technical questions correctly, it is enough. For quantatitative R.S Agarwal is more than enough.practice problems on cubes .

2.The second round is **technical interview.** Here they ask questions from microprocessor (irrespective of dept.), digital electronics,C language etc.

3.The 3rd round is **HR interview.** I was interviewed by an aged HR manager. He asked me questions like tell me something about urself, achievements, any problem in relocation. I believe if u r confident this round is not a hurdle.

Finally they gave a big form to fill up to the selected candidates. I was one of them. Total 14 students from 6 colleges were selected. I was the only one from my college. So best of luck to everybody.

TATA ELXSI PAPER ON 5th MARCH AT BHOPAL

Hey guys,

i m Ankit khare from Truba institute Bhopal. On 6th March i got selected in TATA ELXSI open campus drive. The written test was as usual conducted by merittrac having 4 sections,

1.**Verbal** (RC,usage of articles n prepositions n correct trenses)
2.**Attention in detail** (biggggg nos n u need to find exact match frm the 4 options, and a lot questions on $ @ types....i.e. if 1=$ n 0= @...that
types..)
3.**Analytical Ability**(NOT A SINGLE R.S typesss questions!!! typical LR portion,like Venn diags,selection questions,Cubes questions,age questions....not tough one's)
4.**Technical section**(I m frm CS so in my ppr,there were questions frm C/C++,O.S,DATA com,1 frm CG n few frm god knows where! the questions were not tough but it was like u can come down to final 2 options but it was really difficult to decide d rite one out of two...no need to go into depth of subjects for this section,but be sure of only basics..questions were like:

int n=10

{

int n=20

printf("%d,n)

will this n be global or local...

in O.S questions were on kernel,debugger n i m not strong at that part.... Data com was really easy...direct question on OSI model... 1 question was from DCS...some logic gate question.. i can recall only this much rite nw....

rest for written u can refer to previos papers bcoz d pattern was same....no -ve marking...u better refer to www.meritrac.com, u can find the topics of types of questions.........out of 700, 90 cleard this round n i was one of them :)

Next day was interview day:
TATA ELXSI is known to eliminate students frm tech interview...i was asked to make a no of programs frm C/C++ like call by refrence progs,link list,doublyy link list deletion,stack pointers,i m strong in programming so no problem till here...then he asked questions frm O.S,but not ny theoratical questions,he asked in a very general way,like wat did u learnt frm WinXp,wats d benefit of UNIX n LINUX over Win..etc....then frm data com,TCP/IP n hw it is better than OSI,again not a theoratical answer he asked fr my opinion,similarly frm DBMS,NOTHING FRM CG.....etc etc then he asked me to wait outside....after 2 min he came out n said.."Ankit,move fast for the HR round :)" .the tough part was over

IN HR:
2 ladies took my HR n i was very comfortable within 5 min,although the questions were

typical HR questions like descrive urself,justify ur objective,ur strengths.....etc etc ....but it was more like a talk b/w me n HR rather than interview....she finally asked ny problem in moving to Bangalore,told me abt d 2 yr bond....then she finally stood up n said "Congratulations Ankit,U r selected,now u r IN TATA ELXSI" n then she gave me a form to fill up....:)

Out of 90 ppl, they took only 16 ppl.its not like it depends on their requrement,they r actually looking for quality...if they'll find that in 700 students they'll take them all,but they wont compromise wit wat they want!

TATA ELXSI PAPER ON 25th FEBRUARY AT HYDERABAD

Hi friends I am chaitanya got placed in TATA ELXSI held at Holy Mary Institute of Engg & Tech at Hyderabad.

The selection process consisted of Written test, Tech interview followed by HR interview.

Written Test :
1. English
2. Analytical
3. Mathematical ability
4. Technical (Totally Electronics)

If u r good at english then english is no problem 4 u. Just they concentrated on articles, prepositions, synonyms (context based) and 2 passages.

Analytical part consisted of Data sufficiency, Venn diagrams (very imp) , cubes (very imp), blood relations, small puzzle type questions. Unless u hav sufficient prep u cannot write this section. Prep venn diag & cubes, u will definitely get thru.

The 3rd section is totally IQ based questions like comparisons, arithmetic calculations and finally 2 case studies based on which the questions are framed. For case studies dont think on ur own and give ur own answers, what ever is there in the description go thru it and if even a single condition is not satisfied u must mark none of the above (as required).

The Technical test is totally Electronics based. No C, C++ (bla bla bla). No IT. This is an Electronics based company, so be prepared with all the electronics subjects. They concentrated mostly on LDIC, Micro-Processors, Digital & Analog, Networks (1 question to find current) etc.

There is the end for written test. Out of 1000 plus people only 139 got short listed and the interviews were conducted the next day.

Interview :

I believe interview is only to know abt oneself. If u hav good communication skills and good grip on the subject u will definetely get thru.

Here they concentrated mainly on Micro-processors (Pin diagram, Block diagram, flag registers, Memory interfacing), Op-Amps (defn, clippers, Inverting & Non Inverting derivations, applications), C & C++ (very rare), Compiler, Interpreter and some puzzles like find the angle b/w the min hand and hr hand if it is 5'o clock etc.

HR was simple and they tested on questions like :

What r ur career objectives ?
What improvement needs?
Do u hav any company at hand?
etc....

If u clear the HR round u will be given the form. U need to fill in a lot of information (it takes nearly 40 min to fill the form).

Thats it. There is the end of selection process. Only around 42 people out of 139 short listed got selected.

All the best friends, hav good preparation and crack it. Lets meet at TATA ELXSI.

TATA ELXSI PAPER ON 17th FEBRUARY AT BANGALORE

hi....iam maninder reddy from hyderabd ....studying final year B.Tech in princeton coll of Engg and Tech..... i attended TATA ELXSI on 17th Feb ....iam very happy to say i got placed in TATA ELXSI.........

whole process consistes of

1)Written test
2)Interview

WRITTEN TEST:

totally around 1200 ppl are attended the process ....and around 170 to 180 ppl are cleared the written including me....... comes to the test ...it was conducted by MERITTRACK ppl....

paper consists of FOUR(4) section .....

1)Verbal ability(25qs.............20min)
2)Analytical ability(25qs ....25 min)
3)Attention to details(20qs............20min)
4)Technical(30qs...................30min)

Verbal Ability:
this section consists of 20 qs time allotted is 20 min.
section includes
a) Articles..........5qs
b) synonyms.......5qs
c) Prepositions....5qs
d) passages ....there r 2 passages ..each consists of 5 qs..total 10qs

very simple qs ....the only thing is time mangement...............

Analytical Ability:
after 20 min they will provides this section consists of 25 qs .all r very simple.
1)Vennu diagram problem.........consists of 5 qs
2)Data sufficiancy qs................5 qs
3)reasoning qs consists of...................5 qs
4)and qs like 1 represented by $.........0 represented by *...........then
a) LCM of 2 ,8,32 is_____
b)2^4 is _____
like these qs very simple
5)Cube problem
there are 6 cubes arranged in two rows,3 each ...one on the other .now the middle one of
the top layer is removed. and other top layer cubs are cut into 27 small cubes each. and
the bottom layrr cubes are cut into 64 small ubes each. now the whole structure is painted.
the qs are

a)how many cubes are there with one face painted?
ans:106

b) how many cubes are there with 2 face painted?
c) how many r painted with 3 faces?
d) how many r painted with 4 faces?   ans :0
e) how many with no face painted ?ans:total cubes -sum of above obtained
cubes(1ans+2ans+3ans+4ans)

Attention To Details:
this the section where v can score max........very very simple qs.........consists of 20
qs ....min 20

1)345453.6453   2)3454534.6453   3)345453.6453
which two options are same?

just like that there are 5 qs....

2)next 5 qs consists of pattern
 + represents -,- represents *,* represents /,and / represents +,then which of the following is true?
they r given 4 options ....these r the some what time taking ......be fast ...but very easy

3)next 5 qs are condition based ques.....like
A company recruiting candidates .they given some eligibity criterias.......and asked 5 qs who r eligible...like wise qs
4)next 5 qs r of same model

Technical:
there r two(2) seperate papers ....one for CSE and another for ECE consistes of 30 qs ....in 30 min ....
If we have basics ...then we can do this easily. qs includes......Digital electronics,EDC,OPAMPS,NETWORKS,COMMUNICATION,2qs from C......etc ....time is very sufficiant........be cool ....

They displayed the names those who r cleared the written just after 1hr...the list includes my name ......
the next was **the interview.......**
interview consistes of 2 rounds.....
1)Technical
2)HR

Technical interview:
My chance came at 3 'o clock.there r nearly 10 to 15 panels. in my panel there r 2 interviewers..they didnt asked any HR qs ....direct entered into Technical
i was asked qs from EDC,Digital Electronics,Op-amps,Microcontroller(my project is based on that only),abt project,ADC ,DAC...etc
and another interviewer asked qs from C like ....write aprogram for factorial of a no.
and asked to write the same program in assemgly laung(micro controller)........i learnt the prg during my project and written ....
that ends my Technical round ......and i was said to wait out side ......and i did the same ....
and one person came and said u cleared this round ,,......and taken me to HR round ......

HR :
For me hr was madam.........
she was so cool ......and asked
tell me abt ur self ?......i prepared well and said the same thing
abt strenths?........explained well...i said strenth as analytical ability then she asked to give an example............i givn ....some example
abt family background?
abt hobbies ?...which r wriiten in resume....i written playing cricket then she asked qs in that field ....

and some more simple qs.....
finally asked abt company (TATA ELXSI)........i explained well......

after that she said location where it located , salary package(3 lak per annam),and
agreement(2 years)...and asked r u willing to sign the agreement ......i said yes...
then she asked any qs ?
then asked one ....that do v need to learn any laung(s) by the time of joining ?
then she said no ,no need ....

that ends my HR ....and she was given a form to fill ....the form will be given to those
who r got selected ...... and given one letteroId of compny regarding selection of mine.......

so finally i got selected in good company............

so all the best for all........